

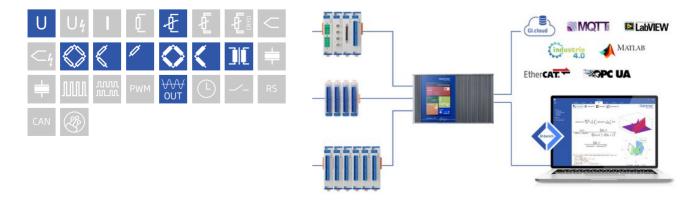
Measurement module for analog inputs and SSI

Q.bloxx XE is a new addition to the Q.series product family - the ideal EtherCAT DAQ solution for widely distributed installations that require higher performance and custom sensor terminations. Q.bloxx XE measurement modules possess integrated signal conditioning and arithmetic functions, packaged in modular, DIN Rail mountable enclosures that easily snap together for system expansion and are capable of measuring up to 100 kHz per channel with short cycle times and low jitter for accurate synchronization.

- RS-485, 2-wire, EtherCAT (LVDS)
- FoE (file access over EtherCAT, ETG.1000.5) and CoE (CAN over EtherCAT, ETG.50001.1)
- Configurable PDO mapping to optimize the data throughput
- Electromagnetic Compatibility according to EN61000-4 and EN55011
- Power supply 10 ... 30 VDC and DIN rail mounting (EN60715)

Key Features

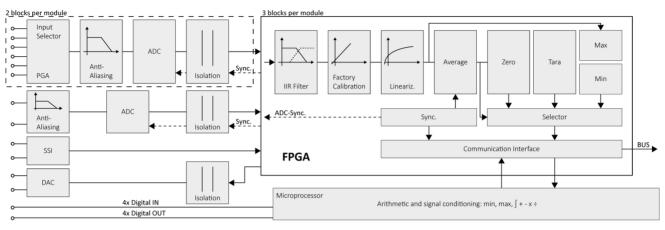
- 2 galvanic isolated universal input channels strain gage and inductive half- and full-bridges, LVDT, RVDT quarter-bridge with completion terminal
- 1 galvanic isolated analog input channel
 10 VDC voltage measurement
- Synchronus Serial Interface (SSI) for absolute Encoder or Temposonics[®]
- 1 Analog output channel
 voltage (±10 VDC) configurable
- 4 digital inputs and outputs status, trigger, tare, alarm, command
- Galvanic isolation
 500 VDC channel-to-channel-to-power for all analogue inputs



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Block diagram



Technical Data

Terminal assignment DSUB 15 HD female

	1	DO 1	1	n.c.	1	AIN +				— 1 <u>—</u>	
	2	DO 2	2	EXC -	2	Ain -			1,2,3,4		5
	З	DO 3	З	EXC+	З	n.c.	5~ 10 [.]		Dout		Uout
	4	DO 4	4	n.c.	4	Clock	-		_10V	└ <u></u> +V	10
	5	AOut +	5	SIG +	5	Clock	+	2		— 2, 3—	
	6	+V	6	n.c.	6	n.c.			3	3	
	7	VCC	7	SEN -	7	n.c.		3		10	
~	+ 8	n VO	~	SEN + 🗸	+ 8	n.c.			2	~2	
	9	0V	9	n.c.	9	Data	-		Ξ	Ξ	
	10	A0ut -	10	SIG -	10	Data -	F		5	10	
	11	DI 1	11	n.c.	11	n.c.			2	×2	
	12	DI 2	12	n.c.	12	n.c.				— 4 <u> </u>	
	13	DIз	13	n.c.	13	n.c.				5 15 10	
	14	DI 4	14	n.c.	14	OV				SSI Occk- Data	
	15	n.c.	15	n.c.	15	+V			2	14 14	

Signal Conditioning

Anti-aliasing filter	5 kHz 5th order (DC excitation) 1 kHz 5th order (CF excitation)		
Digital filters	IIR, low-pass, band-pass, 4th order, 1 Hz to 1 kHz in steps 1, 2, 5		
Averaging	configurable or automatic according to the user-de	fined sample rate	



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Universal Input

Channels	2					
Accuracy	0.02 % typical					
	0.05 % in controlled environment					
	0.1 % in industrial are	а				
Repeatability	0.01 % typical (within	24 h)				
Input impedance	> 10 MΩ					
Isolation voltage	500 VDC channel to ch	annel to power				
Sensor type	DC resistive full-, quar	ter- and half-bridge, pr	essure sensor			
	4.8 kHz carrier frequency mode inductive full-, quarter- and half-bridge, LVDT and RVDT					
Sensor connection	quarter-bridge		3-wire with interna	3-wire with internal 350 Ω bridge completion		
	half-bridge		3- or 5-wire for cab	le-length compensation		
	full-bridge		4- or 6-wire for cab	4- or 6-wire for cable-length compensation		
internal Shunt resistor	100 kΩ, Vexc+ - Vsig+					
Sensor excitation (selectable)	DC: 5 VDC	CF: 5 Veff	DC: 2.5 VDC	CF: 2.5 VDC		
Allowable sensor resistance	> 300 Ω	> 300 Ω	> 100 Q	> 100 Q		
Input range (user selectable)	±1.25 mV/V	±1.25 mV/V	±2.5 mV/V	±2.5 mV/V		
	±2.5 mV/V	±2.5 mV/V	±10 mV/V	±10 mV/V		
	±10 mV/V	±10 mV/V	±20 mV/V	±20 mV/V		
	±20 mV/V	±20 mV/V	±50 mV/V	±50 mV/V		
	±50 mV/V	±50 mV/V	±100 mV/V	±100 mV/V		
	±100 mV/V	±100 mV/V	±200 mV/V	±200 mV/V		
	±200 mV/V	±200 mV/V	±1000 mV/V	±1000 mV/V		
	±1000 mV/V	±1000 mV/V	±2000 mV/V	±2000 mV/V		
Temperature influence Offset drift	<0.2 µV / 10 K (2.5 mV/V input range)					
Temperature influence Gain drift	<0.05%/10K					
Long-term drift	<0.2 µV/V / 24 h					
	<2 µV/V / 8000 h					
Linearity error	<0.02 % FS					
Noise voltage at 10 Hz	· <0.3 μV/V					
Noise voltage at 100Hz	z <1μV/V					



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Voltage Input

Channels	1			
Measurement voltage	Range	Accuracy	Resolution	
	±10 V	±2 mV	±1,2 μV	
Accuracy	0.02 % typical			
	0.05 % in controlled environment			
	0.1 % in industrial area			
Repeatability	0.01 % typical (within 24 h)			
Input impedance	>1 MΩ			
Isolation voltage	500 VDC channel to channel-to-power			
Temperature influence Offset drift	<0.2 µV / 10 K (2.5 mV/V input range)			
Temperature influence Gain drift	<0.05 % / 10 K			
long-term drift	<0.2 µV/V / 24 h			
	<2 µV/V / 8000 h			
linearity error	<2.00 % FS			
Noise voltage at 10 Hz	<0.3 µV/V			
Noise voltage at 100 Hz	<1 µV/V			

Voltage Output

Channels	1
Galvanic isolation	250 VDC channel to channel-to-power
Output voltage	±10 VDC
Accuracy	0.02 %
Resolution	16-bit
Sample rate	20 kHz
Allowable load resistance	> 2kΩ
Temperature influence Offset drift	<1mV/10K
Temperature influence Gain drift	<0.05% / 10 K
Noise voltage at 10 Hz	< 2mV at 10 hZ
Long-term drift	<1 mV/24 h
	<2.5 mV / 8000h

Analog/Digital-Conversion

Resolution	18-bit
Sample rate	20 kHz
Modulation method	SAR



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Digital Inputs

Channels	4
Туре	status
Input voltage	max. 30VDC
Input current	max 2 mA
Threshold (Programmable)	TTL or EN61131-2, Type 1
Logic voltage "O"	-3 to 5 VDC (EN61131-2, Type 1)
Logic voltage "1"	11 to 30 VDC (EN61131-2, Type 1)

Digital Outputs

Channels	4
Туре	Status
Contact	Open drain p-channel MOSFET (short circuit proof)
Output voltage	5 to 30 VDC (external supply required)
Load capacity	30 VDC / 500 mA (resistive load capacity)

Power Supply

Input voltage	10 to 30 VDC, overvoltage and overcurrent protection		
Power consumption	approx 6 W		
Input voltage influence	<0.001 %/V		

Environmental

Operating temperature	-20°C to +60°C
Storage temperature	-40°C to +85°C
Relative humidity	5 % to 95 % at 50°C, non-condensing

Mechanical information

Material	Aluminum and ABS
Measurements (W x H x D)	30x 145 x 135mm
Weight	approx. 500 g

Ordering Information

Article number 702118

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